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	Class	Subclass
ISSUE CLASSIFICATION		

U.S. UTILITY Patent Application

PATENT DATE

O.I.P.E. (2) *Am*
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APPLICATION NO. 09/658861	CONT/PRIOR D F	CLASS 2 430	SUBCLASS 597	ART UNIT 0005 2812	EXAMINER Nguyen, H
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Hikaru Yoshitaka

Semiconductor device and method of manufacturing the same

PTO-2040
12/99[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Primary Examiner) (Date)			ISSUE FEE	
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